

10029 U.S. PTO
10/072743

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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10072743	02/07/2002	17429	827	2831	D. Nguyen

****APPLICANTS:** Harrison Ronnie; Corisis David; 3729

****CONTINUING DATA VERIFIED:**
THIS APPLICATION IS A CON OF 09/004,214 01/09/1998 PAT 6,362,426

**** FOREIGN APPLICATIONS VERIFIED:**

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO. 3758.2US (97-0153.1)
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		
Verified and Acknowledged Examiners's initials		
TITLE: Reduced load frame method of fabricating		

An integrated Circuit Package

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs.Drwg.
		Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
		PREPARED FOR ISSUE	
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